

## **AMENDMENTS TO THE SPECIFICATION**

This listing of amended paragraphs in the specification will replace all prior versions of said amended paragraphs in the application:

### **PAGE 8, THE 4<sup>th</sup> COMPLETE PARAGRAPH:**

(Currently Amended) Referring to ~~Fig. 6~~ Figs 6a - 6h, the pseudo-code....

### **PAGE 8, THE 5<sup>th</sup> COMPLETE PARAGRAPH:**

(Currently Amended) Referring to ~~Fig. 6~~ Figs 6a - 6h, for programmed instruction...

### **PAGE 9, PARAGRAPH 3:**

(Currently Amended) Referring to ~~Fig. 6~~ Figs 6a - 6h, for programmed instruction....

### **PAGE 9, PARAGRAPH 4:**

(Currently Amended) As shown in ~~Fig. 6~~ Figs 6a - 6h, the CAPTUREDIGITS....

### **PAGE 10, THE 1<sup>st</sup> COMPLETE PARAGRAPH:**

(Currently Amended) Referring to ~~Fig. 6~~ Figs 6a - 6h, for programmed instruction....

### **PAGE 10, THE 2<sup>nd</sup> COMPLETE PARAGRAPH:**

(Currently Amended) Referring to ~~Fig. 6~~ Figs 6a - 6h, for programmed instruction....

### **PAGE 10, THE 3<sup>rd</sup> COMPLETE PARAGRAPH:**

(Currently Amended) Referring to ~~Fig. 6~~ Figs 6a - 6h, for programmed instruction....

**PAGE 11, PARAGRAPH 1:**

(Currently Amended) As shown in Fig. 3 and Fig. 4 the hardware and programmed instruction components detailed in ~~[[Fig.s]]~~ Figs 2a, 2b, and [[6]] 6a - 6h may be integrated...

**PAGE 11, PARAGRAPH 2:**

(Currently Amended) Referring to Fig. 5 for methodology references, and ~~Fig. 6~~ Figs 6a - 6h for programmed instruction...